

Electronic Patent Application Fee Transmittal

Application Number:	10501651			
Filing Date:	10-Jan-2005			
Title of Invention:	On-chip esd protection circuit for compound semiconductor heterojunction bipolar transistor rf circuits			
First Named Inventor/Applicant Name:	Yin Tat Ma			
Filer:	Daniel Lewis Dawes			
Attorney Docket Number:	UC1.PAU.05US			
Filed as Small Entity				
U.S. National Stage under 35 USC 371 Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	2251	1	60	60

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				60